European Conference on EDGE AI Technologies and Applications – EEAI

Conference Program

21-23 October 2024, Cagliari, Sardinia, Italy

EDGE
AI
Conference Program

The European Conference on EDGE AI Technologies and Applications – EEAI 2024

Cagliari, Sardinia, Italy, 21-23 October 2024
Venue: Hotel Regina Margherita, Viale Regina Margherita, 44 - 09124 Cagliari, Sardinia, Italy

Monday 21 October 2024 – Day 1

08.30 – 09:00 **Welcome and Registration**

09:00 – 10:30 **Plenary Session I (Room Castello): Edge AI Technology Advancements**

PIONEERING THE FUTURE OF EDGE AI
Ovidiu Vermesan, SINTEF, Norway

A SUSTAINABLE AI-DRIVEN TRANSFORMATION THANKS TO EDGE AI
Alessandro Cremonesi, Executive Vice President, Chief Innovation Officer General Manager, System Research and Applications STMicroelectronics

Project Pitch Presentations – NEUROKIT2E, dAIEDGE, EdgeAI-TRUST, H2TRAIN, ANDANTE (Room Castello)

10:30 – 11:00 **Morning Coffee/Tea Break (Room Terrazza)**

11:00 – 12:30 **Parallel Sessions**

Session A01 (Room Stampace): EDGE AI TOOLS AND METHODS.
Session Co-chairs: *Mario Diaz Nava, STMicroelectronics, France; Tommie Kerssies, Eindhoven University of Technology, The Netherlands.*

Presentation A01-1: RECENT TRENDS IN EDGE AI: EFFICIENT DESIGN, TRAINING AND DEPLOYMENT OF MACHINE LEARNING MODELS. *Maen Mallah, Fraunhofer Institute for Integrated Circuits, Germany.*

Presentation A01-2: AIDGE-GUI: A GRAPHICAL USER INTERFACE FOR AI ON THE EDGE. *Fabian Chersi, CEA-LIST, France.*

Presentation A01-3: A NEW SOFTWARE APPROACH TO EXECUTE AI/ML INFERENCE FOR HIGH-RELIABILITY APPLICATIONS ON SPACE QUALIFIABLE HARDWARE. *Pablo Ghiglino, Klepsydra Technologies, Switzerland.*

Presentation A01-4: EDGE AI FOR BATTERY MANAGEMENT: EFFICIENT SOC AND SOH PREDICTION FOR LI-ION BATTERIES WITH NEURAL NETWORKS. *Silvester Sabathiel, Silicon Austria Labs, Austria.*

Session B01 (Room Castello): EDGE AI MACHINE VISION.
Session Co-chairs: *Alain Pagani, DFKI, Germany; Ana Pinzari, Institute of Engineering Univ. Grenoble Alpes, TIMA, France.*

Presentation B01-1: OPTIMIZED TRAINING FOR WOOD DEFECT CLASSIFICATION. *Angelo Genovese, Università degli Studi di Milano, Italy.*

Presentation B01-2: DISCOVERING AND CLASSIFYING DIGITAL AND WOODEN INDUSTRIES PRODUCTS' DEFECTS AT THE EDGE BY A YOLO/RESNET-BASED APPROACH AND BEYOND. *Alessandro Strano and Robin Faro, Deepsensing srl, Italy.*

Presentation B01-3: MULTI-STEP OBJECT RE-IDENTIFICATION ON EDGE DEVICES: A PIPELINE FOR VEHICLE RE-IDENTIFICATION. *Tomass Zutis, Institute of Electronics and Computer Science, Latvia.*

Presentation B01-4: PRESENTATION.

12:30 – 13:30 **Networking Lunch (Room Terrazza)**

13:30 – 15:00 **Parallel Sessions**

Session C01 (Room Stampace): RISC-V-BASED EDGE AI HARDWARE ACCELERATION.
Session Co-chairs: *Paolo Meloni, Università degli Studi di Cagliari, Italy; Eike Schultz, University of Lübeck, Germany.*

Presentation C01-1: A RECONFIGURABLE-BASED HETEROGENEOUS AND HIGHLY PARALLEL PROCESSING PLATFORM FOR SAFE AND SECURE AI. *Konstantinos Georgopoulos, Technical University of Crete and National Technical University of Athens, Greece; Iakovos Mavroidis, Technical University of Crete and Exascale Performance Systems (EXAPSYS) plc., Greece.*

Presentation C01-2: AUTOMATIC MODEL TRAINING, COMPRESSION, AND QUANTISATION TOOLS FOR RISC-V ACCELERATORS. *Timothy Llewellynn, Bonseyes, Switzerland.*

Presentation C01-3: TRISTAN – EMBRYO OF A EUROPEAN RISC-V ROADMAP. *Patrick Pype, NXP Semiconductors, Belgium.*

Presentation C01-4: SYNTZULU: A TINY RISC-V-CONTROLLED SNN PROCESSOR FOR REAL-TIME SENSOR DATA ANALYSIS ON LOW-POWER FPGAs. *Gianluca Leone, Università degli Studi di Cagliari, Italy*

Session D01 (Room Castello): EDGE AI ACCELERATORS.
Session Co-chairs: *Hana Krichene, CEA-LIST, France; Peet van Tooren, Almende, The Netherlands.*

Presentation D01-1: PLATFORM-INDEPENDENT FPGA AI MAPPING TOOL BASED ON FINN. *Remo Senekowitsch, Zurich University of Applied Sciences (ZHAW), Switzerland.*

Presentation D01-2: SPIKING AND ARTIFICIAL NEURAL NETWORK ACCELERATORS IMPLEMENTED IN ANDANTE. *Mario Diaz Nava, STMicroelectronics, France.*

Presentation D01-3: FPGA ACCELERATION FOR SAR IMAGERY-BASED SEA ICE MAPPING. *Rashed Al Koutayni, DFKI Kaiserslautern, Germany.*

Presentation D01-4: PRESENTATION

15:00 – 15:30 **Afternoon Coffee/Tea Break (Room Terrazza)**

15:30 – 17:00

Parallel Sessions

Session E01 (Room Stampace): BENCHMARKING EDGE AI MODELS.

Session Co-chairs: *Gerardo Daalderop, NXP Semiconductors B.V.; Mohammed Abuteir, TTTech, Austria.*

Presentation E01-1: CHALLENGES AND PERFORMANCE OF SLAM ALGORITHMS ON RESOURCE-CONSTRAINED DEVICE. *Calvin Galagain, French Alternative Energies and Atomic Energy Commission (CEA), France.*

Presentation E01-2: ONLINE AI BENCHMARKING ON REMOTE BOARD FARMS. *Maïck Huguenin, Applied Science University Western Switzerland, Switzerland.*

Presentation E01-3: PCA-BASED CONFIDENTIALITY PRESERVING METHOD FOR REAL-TIME SOFT-FAILURE DETECTION IN OPTICAL NETWORKS. *Azarm Yeganehfallah, Scuola Superiore Sant'Anna, Italy.*

Presentation E01-4: PRESENTATION

Session F01 (Room Castello): OPTIMISATION METHODS AND TECHNIQUES FOR NEURAL NETWORKS.

Session Co-chairs: *Mario Diaz Nava, STMicroelectronics, France; Pavel Smrz, Brno University of Technology.*

Presentation F01-1: ENHANCING SUSTAINABILITY IN WATER RESOURCE MANAGEMENT: OPTIMIZING NEURAL NETWORK ARCHITECTURES FOR ACCURATE PREDICTION OF WATER STRESS IN EUROPEAN COUNTRIES. *Manal Jammal, IoT Digital Innovation Hub, Spain, and Laura Sanz-Martín, University of Salamanca, Spain.*

Presentation F01-2: PRESENTATION

Presentation F01-3: MARKET PATHWAYS FOR CLOUD EDGE IOT IN KEY SECTORS TRANSPORTATION, AGRICULTURE, ENERGY AND MANUFACTURING. *Elisabeth Steimetz and Inessa Seifert, VDI/VDE-IT GmbH, Germany.*

Presentation F01-4: EDGE-ORIENTED DISTRIBUTED ARCHITECTURE BASED ON ROAD-SIDE-PERCEPTION-UNITS (RSPUS) TO SUPPORT A FEDERATED LEARNING APPROACH FOR URBAN AIR QUALITY PREDICTIONS. *George Bravos, ITML, Greece.*

End

18:00

Conference Cocktail - Grotta Marcello, Cagliari

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Tuesday 22 October 2024 – Day 2

08.30 – 09:00

09:00 – 10:30 **Plenary Session II (Room Castello): European Edge AI Ecosystem**

AI AND SAFETY AS KEY ELEMENTS FOR A SOFTWARE DEFINED VEHICLE.
Patrick Pype, CTO, NXP Semiconductors, Belgium

OVERVIEW OF THE INDUSTRIAL ASSOCIATIONS ACTIVITIES
Elisabeth Steimetz, EPoSS Industry Association, VDI/VDE-IT GmbH, Germany
Paolo Azzoni, Inside Industry Association, the Netherlands

Project Pitch Presentations - AI4CSM, A-IQ Ready, LoLiPoP-IoT, ARCHIMEDES, CLEVER. SMARTY (Room Castello)

10:30 – 11:00 **Morning Coffee/Tea Break (Room Terrazza)**

11:00 – 12:30 **Parallel Sessions**

Session A02 (Room Stampace): TRANSFORMERS AT THE EDGE.
Session Co-chairs: *Patrick Pype, NXP Semiconductors, Belgium; Konstantinos Georgopoulos, National Technical University of Athens, Greece.*

Presentation A02-1: EFFICIENT QUANTIZATION OF VISION TRANSFORMERS FOR MOBILE DEVICES INFERENCE: CHALLENGES AND POST-TRAINING STRATEGIES. *Martyna Poreba, CEA-List, Université Paris-Saclay, France.*

Presentation A02-2: OPTIMIZING VISION TRANSFORMERS FOR EDGE DEPLOYMENT: HYBRID TOKEN REDUCTION FOR EFFICIENT SEMANTIC SEGMENTATION. *Martyna Poreba, French Alternative Energies and Atomic Energy Commission (CEA), France.*

Presentation A02-3: OPTIMIZING COMPILATION STRATEGIES WITH DeepCodeGraph: A GRAPH NEURAL NETWORK APPROACH TO SOURCE CODE ANALYSIS. *Francesco Barchi, University of Bologna, Italy.*

Presentation A02-4: EXPLORING THE BENEFITS OF VISION FOUNDATION MODELS FOR UNSUPERVISED DOMAIN ADAPTATION. *Tommie Kerssies, Eindhoven University of Technology, The Netherlands.*

Session B02 (Room Castello): NEUROMORPHIC HW ARCHITECTURES.
Session Co-chairs: *Björn Debaillie, IMEC, Belgium; Fabian Chersi, CEA-LIST, France.*

Presentation B02-1: NEUROMORPHIC ARCHITECTURE FOR IOT EDGE WATER MANAGEMENT. *Mugdim Bublin and Heimo Hirner, University of Applied Science FH Campus Wien, Austria.*

Presentation B02-2: MEMRISTIVE ASSOCIATE-LEARNING ARCHITECTURE FOR HIGH-ACCURACY ULTRALOW POWER EDGE LEARNING. *Sayani Majumdar, Tampere University, Finland.*

Presentation B02-3: ENABLING NEAR-SENSOR AI WITH LOW-POWER PROCESSING: AN OPEN-HARDWARE EXPERIENCE. *Luca Martis, Università degli Studi di Cagliari, Italy.*

Presentation B02-4: PRIVACY-PRESERVING DISTRIBUTED LEARNING AT THE EDGE THROUGH APPROXIMATE COMPUTING. *Ihsen Alouani, CNRS, Université Polytechnique Hauts-de-France.*

12:30 – 13:30 **Networking Lunch (Room Terrazza)**

13:30 – 15:00 **Parallel Sessions**

Session C02 (Room Stampace): EDGE AI HARDWARE-SOFTWARE CO-DESIGN METHODS AND TOOLS.

Session Co-chairs: *Marcello Coppola, STMicroelectronics, France; Angelo Genovese, Università degli Studi di Milano, Italy.*

Presentation C02-1: MANDALA: OPTIMISED ARTIFICIAL INTELLIGENCE HPDP INTERFACE USING KLEPSYDRA AI STREAMING. *Pablo Ghiglino, Klepsydra Technologies, Switzerland.*

Presentation C02-2: EDGEAI: LEVERAGING SNNS, TRANSFORMERS, AND AUTOENCODERS FOR NEAR-SENSOR INTELLIGENCE. *Paola Busia, Università degli Studi di Cagliari, Italy.*

Presentation C02-3: AUTOMATIC HARDWARE-AWARE DESIGN, OPTIMIZATION, AND DEPLOYMENT OF DEEP LEARNING MODELS ON HETEROGENEOUS EDGE DEVICES. *Matteo Risso. Politecnico di Torino, Italy.*

Presentation C02-4: PREVAIL - OPEN ACCESS PILOT LINE FOR EDGE AI CHIP PROTOTYPING. *Sergio Nicoletti, CEA, France.*

Session D02 (Room Castello): RECONFIGURABLE EDGE AI.

Session Co-chairs: *Heimo Hirner, University of Applied Science FH Campus Wien, Austria; Alberto Faro, Deepsensing SRL, Italy.*

Presentation D02-1: SWARM NETWORKING FOR SMART FACTORY: THE SMARTEDGE SOLUTION. *Mohamed Mtiri, CNIT, Italy.*

Presentation D02-2: AI-BASED DYNAMICALLY CONTROL AND OPTIMIZE LIGHTING CONDITIONS BASED ON AN AMBIENT INPUT. *Tore Karlsen, ProLux AS, Norway.*

Presentation D02-3: AN ADAPTIVE FORECASTING PLATFORM ENABLING DIGITAL TWINNING. *Luca Valcarenghi, Scuola Superiore Sant'Anna, Italy.*

Presentation D02-4: CLOUD-EDGE CONTINUUM OF AI - ENABLED BODY SENSORS FOR MULTISPORT TRAINING, HEALTHY LIVING, AND REMOTE PATIENT MONITORING IN EUROPE. *Juan A Montiel-Nelson, Univ. Las Palmas, Spain.*

15:00 – 15:30 **Afternoon Coffee/Tea Break (Room Terrazza)**

15:30 – 17:00 **Parallel Sessions**

Session E02 (Room Stampace): EDGE AI ARCHITECTURES, FRAMEWORKS, AND MODEL OPTIMISATION.
Session Co-chairs: *Iakovos Mavroidis, Technical University of Crete, Greece; Martyna Poreba, CEA-List, France.*

Presentation E02-1: A NOVEL HIERARCHICAL APPROACH TO PERFORM ON-DEVICE ENERGY EFFICIENT FAULT CLASSIFICATION. *Devesh Vashishth, Ferdinand Steinbeis Institute, Germany; Julio Wissing, Fraunhofer Institute for integrated circuits IIS, Germany.*

Presentation E02-2: EDGE AI HARDWARE ACCELERATION FOR CRITICAL SYSTEMS: FROM FPGAs AUTOMATIC HARDWARE GENERATOR TO VERSATILE CGRAs SYSTEMS. *Pietro Nannipieri, University of Pisa, Italy.*

Presentation E02-3: EFFICIENT EDGEAI COMPUTER VISION THROUGH VARIOUS MODEL OPTIMIZATION TECHNIQUES. *Hiram Rayo Torres Rodriguez and Gerardo Daalderop, NXP Semiconductors B.V., The Netherlands.*

Presentation E02-4: EVALUATION OF YOLO MODEL FOR AGRI-FOOD REQUIREMENTS AND EDGE-AI SPECIFICATIONS. *Ana Pinzari, Institute of Engineering Univ. Grenoble Alpes, TIMA, France.*

Session F02 (Room Castello): EDGE AI DEPENDABLE SYSTEMS.
Session Co-chairs: *Gleb Radchenko, Silicon Austria Labs; Juan A Montiel-Nelson, Univ. Las Palmas, Spain.*

Presentation F02-1: AN AI-BASED HARDWARE/SOFTWARE WATER HEATER SOLUTION FOR PROCESSING THE ENERGY DATA AT THE EDGE. *Mats Jørgen Øyan, Nxtech AS, Norway.*

Presentation F02-2: FUNCTIONAL AND NON-FUNCTIONAL REQUIREMENTS IN EDGE AI SYSTEMS. *Ovidiu Vermesan, SINTEF, Norway; Alain Pagani, DFKI, Germany.*

Presentation F02-3: DEPENDABLE AI AT THE EDGE. *Eike Schultz, University of Lübeck, Germany.*

Presentation F02-4: A WORKFLOW PROPOSAL FOR SAFE-AI. *Suzana Veljanovska, Zurich University of Applied Sciences (ZHAW), Switzerland.*

End

19:00 – 20:00 **Cagliari City Center Walk Tour**

20:00 **Conference Dinner - Ristorante Sa Schironada, Cagliari**

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Wednesday 23 October 2024 – Day 3

08.30 – 09:00

09:00 – 10:30 **Plenary Session III (Room Castello): Edge AI Tools**

DESIGNING ACCELERATED EDGE AI SYSTEMS WITH MODEL BASED METHODOLOGY.
Petri Solanti, Siemens EDA, Germany

Project Pitch Presentations - AIMS5.0, SC4EU. TRISTAN, REBECCA, EdgeAI (Room Castello)

10:30 – 11:00 **Morning Coffee/Tea Break (Room Terrazza)**

11:00 – 12:30 **Parallel Sessions**

Session A03 (Room Stampace): EDGE AI FEDERATED LEARNING.
Session Co-chairs: *Reiner John, AVL, Austria; Silke Braun, Infineon, Austria.*

Presentation A03-1: BAYESIAN APPROACHES TO UNCERTAINTY ESTIMATION IN EDGE AI COLLABORATIVE LEARNING. *Gleb Radchenko, Silicon Austria Labs.*

Presentation A03-2: FEDERATED LEARNING APPLIED ON EDGE-AI HARDWARE FOR ELECTRIC VEHICLE RANGE ESTIMATION. *André Baldermann, Ostbayerische Technische Hochschule Amberg-Weiden, Germany.*

Presentation A03-3: FEDERATED LEARNING AND KNOWLEDGE DISTILLATION FOR AT-THE-EDGE EV MONITORING AND PREDICTIVE MAINTENANCE. *Pierluigi Dell'Acqua, Lorenzo Carnevale, and Massimo Villari Università degli Studi di Messina, Italy.*

Presentation A03-4: PIONEERING THE HYBRIDIZATION OF FEDERATED LEARNING IN HUMAN ACTIVITY RECOGNITION. *Federico Montori, University of Bologna, Italy.*

Session B03 (Room Castello): TRUSTWORTHY EDGE AI.
Session Co-chairs: *Markus Tauber, Research Studio Austria; Thomas Gutt, Infineon Technologies AG, Germany.*

Presentation B03-1: THE ACCOUNTABILITY STRIKES BACK: DECENTRALIZING THE KEY GENERATION IN CL-PKC WITH TRACEABLE RING SIGNATURES. *Varesh Mishra, COSIC KU Leuven, Belgium.*

Presentation B03-2: LEGAL REQUIREMENTS FOR TRUSTWORTHY EDGE AI: THE CURRENT STATE-OF-PLAY AND RECENT LEGAL DEVELOPMENTS. *Lydia Belkadi, KU Leuven, Belgium.*

Presentation B03-3: TRUSTWORTHY EDGE AI SOLUTIONS IN AUTONOMOUS VEHICLE PERCEPTION AND SENSOR FUSION. *Ovidiu Vermesan, SINTEF, Norway*

Presentation B03-4: PRESENTATION.

12:30 – 13:30 **Networking Lunch (Room Terrazza)**

13:30 – 15:00

Parallel Sessions

Session C03 (Room Stampace): EDGE AI SYSTEMS APPLICATIONS.

Session Co-chairs: *Silvester Sabathiel, Silicon Austria Labs, Austria; Ihsen Alouani, CNRS, Université Polytechnique Hauts-de-France.*

Presentation C03-1: MODEL SELECTION AND PROMPTING STRATEGIES IN RESOURCE-CONSTRAINED ENVIRONMENTS FOR LLM-BASED ROBOTIC SYSTEM. *Toms Eduards Zinars, Institute of Electronics and Computer Science, Latvia.*

Presentation C03-2: FROM HUMAN EXPERT TO DIGITAL KNOWLEDGE: APPLYING METHODS FOR KNOWLEDGE EXTRACTION. *Silke Braun, Infineon, Austria.*

Presentation C03-3: IMPLEMENTATION OF AN AIOT TEACHING KIT BASED ON LORA TECHNOLOGY AND EDGE AI. *Louis Noaille, Technext, France.*

Presentation C03-4: CONSCIOUS AGENTS INTERACTION FRAMEWORK FOR INDUSTRIAL AUTOMATION. *Polina Ovsiannikova, Aalto University, Finland.*

Session D03 (Room Castello): SUSTAINABLE EDGE AI TECHNOLOGIES AND LIFECYCLE DEVELOPMENT.

Session Co-chairs: *Michael Cernusca, ams OSRAM AG; Modris Greitans, Institute of Electronics and Computer Science, Latvia.*

Presentation D03-1: AI SUSTAINABLE APPLICATIONS - EXPERIENCING DRIVING. *Reiner John, AVL, Austria.*

Presentation D03-2: ARTIFICIAL INTELLIGENCE IN MANUFACTURING LEADING TO SUSTAINABILITY AND INDUSTRY5.0. *Thomas Gutt, Infineon Technologies AG, Germany; Markus Tauber, Research Studio Austria.*

Presentation D03-3: EXPLAINABLE AI LIFECYCLE. *Dinu Purice, Cognition Factory, Germany.*

Presentation D03-4: AI BASED ATTACK DETECTION ON RESOURCE CONSTRAINED SECURE MICRO EDGE DEVICES. *Ujunwa Irogbarachi, NXP Semiconductors, Germany.*

15:00 – 15:30 **Afternoon Coffee/Tea Break (Room Terrazza)**

15:30 – 17:00 **Edge AI Cluster Internal Meeting (Room Castello)**

End

09:00 – 17:00 **Poster Session (Room Terrazza): 21-23 October 2024**

Poster P01: ENABLING RELIABLE COMMUNICATION IN V2X SCENARIOS.
Anja Dakić (1), Benjamin Rainer (1), Thomas Zemen (1), Stefan Jakšić (1), Anamarija Momic (2), Peter Priller (2), Xiaochum Ye3, Guo Nan (3).
(1) AIT Austrian Institute of Technology GmbH, Center for Digital Safety & Security, Giefinggasse 4, 1210 Vienna, Austria; (2) AVL List GmbH, Hans-List-Platz 1, 8020 Graz, Austria; (3) Institute of Computing Technology, Chinese Academy of Sciences, No. 6 Kexueyuan South Road Zhongguancun, Haidan District Bejing, China.

Poster P03: EDGE AI APPLICATIONS IN DIGITAL INDUSTRY.
Silke Braun, Josef-Anton Moser, Infineon, Austria.

Poster P05: EDGE AI APPLICATIONS IN AGRI-FOOD AND BEVERAGE.
Marcello Coppola, STMicroelectronics, France.

Poster P07: EDGE AI APPLICATIONS IN DIGITAL SOCIETY.
Gerardo Daalderop, NXP Semiconductors, the Netherlands.

Poster P09: CHIPS JU EDGEAI PROJECT.
Athanasia Thraskia, ITML, Greece.

Poster P11: FLEXIBLE DNN MAPPING MODEL FOR NOC-BASED DATAFLOW ARCHITECTURES.
William Fabre, Hana Krichene, Rohit Prasad, and Caaliph Andriamisaina, CEA, France.

Poster P13: COMPUTER VISION AT THE EDGE.
Arno van der Weijden, Technolution BV, The Netherlands.

Poster P02: CONTINUOUS-TIME NEUROMORPHIC PROCESSING AT THE SENSOR EDGE.
Amir Zjajo, Sumeet Kumar, Innatera Nanosystems B.V., The Netherlands.

Poster P04: CONTINUOUS OPTIMIZATION AND BENCHMARKING OF AI MODELS ON MICROCONTROLLERS: A TINYMLOPS FRAMEWORK FOR REAL-WORLD APPLICATIONS.
Mattia Antonini, Massimo Vecchio, Fabio Antonelli, Fondazione Bruno Kessler, Trento, Italy.

Poster P06: EDGE AI APPLICATIONS IN ENERGY INDUSTRY.
Ovidiu Vermesan, SINTEF AS, Norway.

Poster P08: EDGE AI APPLICATIONS IN MOBILITY INDUSTRY.
Janis Arents, EDI, Latvia.

Poster P10: NON-VOLATILE MEMORY (NVM) BASED IN-MEMORY COMPUTING (IMC) SOLUTIONS OF ANDANTE PROJECT.
Alexandre Valentian, CEA, France, Mario Diaz Nava, STMicroelectronics, France.

Poster P12: APPLICATION OF DEEP NEURAL NETWORKS FOR FUNDAMENTAL FREQUENCY EXTRACTION IN OCEAN CURRENT METERS USING EDGE COMPUTING ON ULTRA LOW POWER MICROCONTROLLERS.
Juan Montiel-Caminos, Nieves G. Hernandez-Gonzalez and Javier-Sosa. University of Las Palmas de Gran Canaria, Spain.

Presentation P14: NEW MODULAR BUILDING BLOCKS APPROACHES FOR CLASSIFICATION IN REAL WORLD APPLICATIONS IN EDGE AI. *Lucas Mohimont, Université de Reims Champagne-Ardenne (URCA), France.*

17:00 **End**

Welcome to Cagliari, Sardinia, Italy