EDGE AI Newsletter

Volume 8, Issue 1
January 2025

European Conference on EDGE AI

European Conference on EDGE AI Technologies and Applications – EEAI, 21-23 October 2024, Cagliari, Sardinia, Italy

The **European Conference on EDGE AI Technologies and Applications – EEAI** at the Hotel Regina Margherita, Cagliari, Sardinia, Italy, was co-organised by the large-scale KDT JU EdgeAI project in co-operation with the CLEVER, REBECCA, TRISTAN, NEUROKIT2E, LoLiPoP IoT projects, ECSEL JU ANDANTE, AI4CSM, AI4DI, TEMPO and INSECTT projects.

The EEAI conference provide a platform to exchange knowledge and ideas among experts and professionals interested in advances in AI circuits and device design, AI hardware architectures, industrial edge AI technologies, toolchains, and applications.

The conference addressed the edge AI foundations, applications, algorithms, software, communication networks, and systems.

EEAI 2024 allowed showcasing the latest advancements in these areas, providing an arena for debating/identifying future directions and challenges.

The program spanned three days, with a plenary session starting each day and parallel sessions covering four to six topics daily.

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The conference content covered the edge AI technology stack following emerging research and innovation from hardware to software, edge AI frameworks, architectures, algorithms, data types, and methods to applications. The presentations and the technical sessions highlighted the expansion of the micro-, deep-, and meta-edge continuum as part of the novel distributed AI-based computing architectures supporting emerging intelligent autonomous systems and applications across various industrial sectors.

The event gathered more than 130 delegates from academia, researchers, and industry practitioners who exchanged the latest fundamental advances in state-of-the-art practices of edge AI technologies and applications, identifying emerging research topics and defining the future of edge AI research and innovation.

The European Conference on EDGE AI Technologies and Applications was a success, with the participants appreciating the quality of the technical presentations, sessions' content, networking opportunities, engagement with fellow participants, venue and the overall organisation.

The conference achieved its aim to serve as an innovative edge AI platform for exchanging ideas and concepts and feature several leading voices in research and industry supporting the integration of edge AI technologies for advancing future industrial applications.

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EdgeAI General Assembly

EdgeAI General Assembly, 24 October 2024, Cagliari, Sardinia, Italy

The EdgeAI consortium organised its General Assembly on October 24th, 2024, in Cagliari, Sardinia, Italy, coinciding with the EEAI 2024 conference. The meeting brought together over 40 representatives from project beneficiaries, affiliated entities, and associated partners.

The assembly aimed to provide a platform for consortium members to discuss the project's current status and future direction. Presentations highlighted advancements in the 20 project demonstrators across five value chains, with discussions centered on upcoming deliverables, progress updates, and challenges encountered.

Furthermore, the presentations provided an overview of key ecosystem orchestration activities, including communication, dissemination, exploitation, and standardization, along with plans for the upcoming year. A key highlight was the circulation of a questionnaire to gather valuable feedback from partners on the conference, including recommendations for future events.

The EdgeAI general assembly concluded with a questions and answers session that reflected the issues of interest for the consortium beneficiaries.

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EdgeAI 1st Webinar

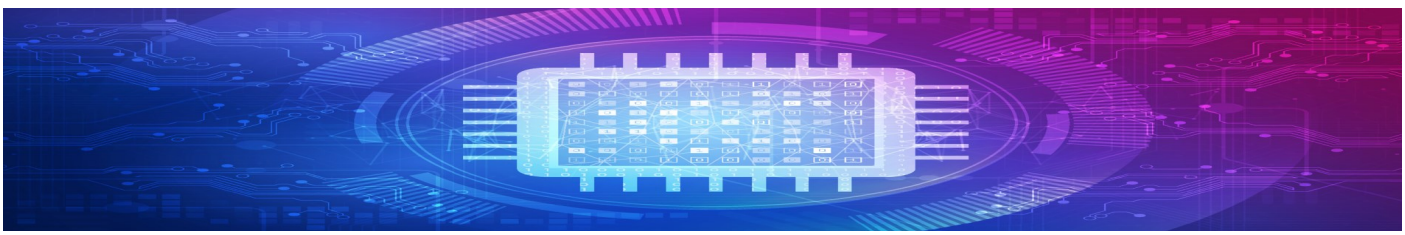
Edge AI Webinar - Edge AI platforms and software environment for application development - NXP AIML Edge Platforms

The webinar introduced NXP Semiconductors' edge AI platforms, software environment for application development, and model-zoo, offering validated starting points for edge AI projects. The event included practical application examples and concludes with a look at industrial research efforts towards optimisation at the edge.

This webinar was organised by the Chips JU EdgeAI – Edge AI Technologies for Optimized Performance Embedded Processing project, aiming at delivering an in-depth overview of edge AI platforms, tools, applications and industrial research and development targeted towards mass-market democratisation of AIML at the edge.

The event successfully concluded with over 60 registered participants and positive feedback from the audience.

Watch the recording on our YouTube channel: Chips JU EdgeAI Webinar - Edge AI Platforms and Software Environment NXP or visit our website: Edge AI Academy - EdgeAI to learn more about Edge AI technologies.



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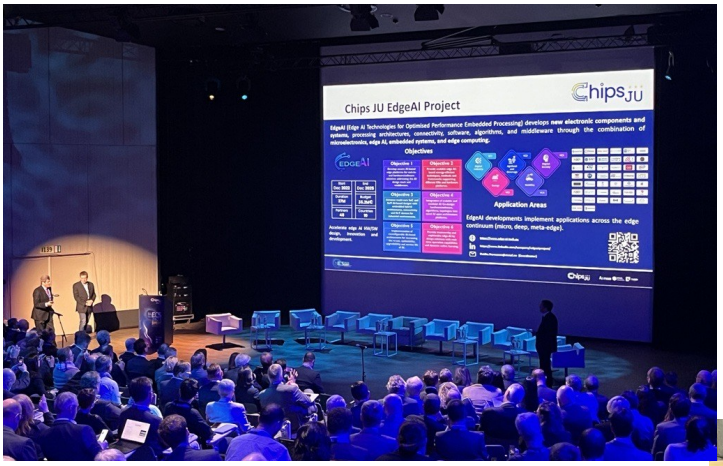
EFECS 2024

EFECS 2024, 5 - 6 December 2024, Ghent, Belgium

EFECS 2024 brought together industry leaders, policymakers, researchers, and innovators to shape the future of Europe's strategic leadership in semiconductor technology.



Beyond the plenary session, the EdgeAI project had a strong presence at the forum. Our dedicated booth, supported by numerous consortium partners, provided valuable insights into the project's progress.

As EFECS 2024 concludes, we remain committed to driving impactful research and innovation towards a brighter future.

A highlight of the forum was the plenary session, where our coordinator Ovidiu Vermesan from SINTEF Digital, delivered a compelling presentation to an audience of 700 people. The talk, "Revolutionising Intelligence – Edge AI Anywhere, Anytime," explored the transformative potential of AI in electronic devices and systems.

Touching on the convergence of technologies such as chiplets, RISC-V architecture, edge generative AI, and heterogeneous integration, the presentation delved into emerging trends like neuromorphic architectures, biomimicry, and the interplay with neuroscience for more efficient computing processing.

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https://edge-ai-tech.eu/

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EdgeAI Key Facts

Start: 01 December 2022

Duration: 37 months

Budget: 35,43 M€

Coordinator: SINTEF AS

Consortium: 48 partners

Consortium

SINTEF, Infineon, ams OSRAM, SoftwareCUBE, TU Wien, imec, NXP, GNT, NEURO Controls, COGNITION FACTORY, IMST, Universität zu Lübeck, scm group, ST, VRANKEN POMMERY, TECHNEXT, cea, Grenoble INP UGA, Université de Reims Champagne-Ardenne, cnrs, Université Polytechnique Hauts-de-France, Χαροκοπειο Πανεπιστημιο Harokopio University, HTS, Intelligent Machines IoT & Cyber Physical Systems DeepSensing S.R.L., Xtremion, Università degli Studi di Cagliari, Alma Mater Studiorum Università di Bologna, Università degli Studi di Milano, Elektronikas un datorzinātnu institūts Institute of Electronics and Computer Science, signify, Technolution, almende, TU/e, Fredrikstad energi, NxtEnergy, PROLUX, nxtech, itml innovation applied, CONVERGENCE, SynSense

Disclaimer

Funded by the European Union. Views and opinions expressed are however those of the author(s) only and do not necessarily reflect those of the European Union or KDT JU. Neither the European Union nor the granting authority can be held responsible for them.

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